

## MINIATURE FIBER OPTIC MEMS SWITCH

## OVERVIEW

The sercalo $s x$ series are miniature opto-mechanical switches for fiber optic communication systems and submodules. The switch is available in latching or nonlatching variants, with $1 \times 1,2 \times 1,2 \times 2$, The switch offers smallest size, ease of integration and the established solid state reliability of Sercalo's MEMS components.
The plastic package is one of the smallest in the industry. It is optimized for low cost production while maintaining high reliability comparable to a solid state device. The component is designed to meet Telcordia 1221 quality standards.

## FEATURES

- $23 \times 10 \times 6 \mathrm{~mm}$ size
- Low Cost
- TTL or CMOS logic
- latching
- $2 \times 2,2 \times 1,1 \times 1$ variants
- single or multimode fiber


## APPLICATIONS

- Protection Switching
- Reconfiguration
- Optical Subsystems
- Array integration


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## DESCRIPTION

In the serealo sx switches the optical switching function is realised by a silicon MEMS chip, on which a mirror can be moved in and out of the optical path by electrostatic actuation. The miniature SX switch is only available in its latching variant where a bistable suspension mechanism keeps the last selected state in power off.
To operate the switch 5 V and 0 V are applied on pins 1 and 2, which are used by the internal DC-DC converter to supply a high voltage for the actuator control. CMOS or TTL logic levels on pins 3-4 control the electrostatic actuator.
To set the switch state in the latching variant, pin 3 respectively pin 4 are set to logic high ( 5 V ) for 20 ms and the corresponding switch state is selected. At rest pins 3 and 4 should be pulled to 0 V and must not be floating.


BAR STATE


TECHNICAL SPECIFICATIONS (for single mode fibres ${ }^{1}$ ) Switch

Wavelength Range ${ }^{1}$ Insertion Loss ${ }^{2}$
Crosstalk
Return Loss
Polarisation Dependent Loss
Repeatability ${ }^{3}$
Switching Time
Durability
Integrated Driver
Supply Voltage Vcc (pin 1)
Current Consumption Icc (pin 1)
Logic Level Low (pins 3, 4)
Logic Level High (pins 3, 4)
Selection Pulse Width
Package
Operation Temperature
Storage Temperature
Size (L×W $\times H$ ) - for single
Size ( $\mathrm{L} \times \mathrm{W} \times \mathrm{H}$ ) - for dual
${ }^{2}$ value @ $25{ }^{\circ} \mathrm{C}$, without connectors. ${ }^{3}$ for constant temperature and polarisation.



Figure 2: spectral response over temperature
Figure 1: Insertion loss distribution



Figure 5: Electrical Schematic Diagram

